H·A

	Application No.	Applicant(s)
Notice of Allowability	10/624,890	SADAKATA ET AL.
	Examiner	Art Unit
	Michael t. Tran	2827
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS therewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIP of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not included will be mailed in due course. THIS
 This communication is responsive to <u>communications date</u> 	<u>d December 18, 2003</u> .	
2. ☑ The allowed claim(s) is/are <u>1-13</u> .		
3. \boxtimes The drawings filed on <u>18 December 2003</u> are accepted by	the Examiner.	
4. Acknowledgment is made of a claim for foreign priority una) All b) □ Some* c) □ None of the: 1. ☑ Certified copies of the priority documents have 2. □ Certified copies of the priority documents have 3. □ Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE"	been received. been received in Application No cuments have been received in this of this communication to file a reply	national stage application from the
noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give	itted. Note the attached EXAMINER	
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") mus (a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	on's Patent Drawing Review (PTO- s Amendment / Comment or in the C	Office action of
Identifying Indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on the drawli he header according to 37 CFR 1.121(ngs in the front (not the back) of d).
 DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT 		
Attachment(s)		
1. ☑ Notice of References Cited (PTO-892)	5. Notice of Informal P	atent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	 Interview Summary Paper No./Mail Date 	
 Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date <u>21503</u> 		
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's Stateme	ent of Reasons for Allowance
of Biological Material	9. Other	
	,	MICHAELTRAN PRIMARY EXAMINER
		COMMUNICATION CARMINER

Page 2

Application/Control Number: 10/624,890

Art Unit: 2827

DETAILED ACTION

1. In response to the Communication dated December 18, 2003, claims 1-13 are active in this application.

Foreign Priority

- 2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)
- (d), which papers have been placed of record in the file.

Information Disclosure Statement

3. The information disclosure statement filed December 18, 2003 has been considered.

Allowable Subject Matter

- 4. Claims 1-13 are allowable over the prior art of record.
- 5. The following is an Examiner's statement of reasons for the indication of allowable subject matter: the prior art of records does not show (in addition to other elements in the claim) the following:
 - Means for setting the number of address signals in each of said plurality of RAMs
 equal to the number of address signals in the largest address space.

Application/Control Number: 10/624,890 Page 3

Art Unit: 2827

Means for setting the number of address signals in each of said plurality of RAMs
equal to the number of address signals in the largest address space when
mapping the address space for test purposes.

- Means capable of changing an address connection between an external input
 address and a chip internal input address according to said differing memory
 spaces, wherein said means enables the number of address signals in each of
 said plurality of RAMs to be set equal to the number of address signals in the
 largest address space.
- Means for applying the same mapping to said differing memory spaces when simultaneously testing said plurality of RAMs; and means for inhibiting access to each individual RAM according to said differing memory spaces.
- Wherein all of said RAMs are tested simultaneously by setting the number of address signals in each of said plurality of RAMs equal to the number of address signals in the largest address space.
- 6. Any comments considered necessary by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

7. Any inquiry concerning this communication or earlier communications from the

Application/Control Number: 10/624,890 Page 4

Art Unit: 2827

examiner should be directed to Michael T. Tran whose telephone number is (571) 272-1795.

8. Any inquiry of a general nature or relating to the status of this application should be directed to Group receptionist whose telephone number is (571) 272-1650.

Michael T. Tran

February 4, 2005

MICHAELTRAN PRIMARY EXAMINER